

Notice of Allowability

Application No.

10/038,753

Examiner

Thomas L Dickey

Applicant(s)

LETOR ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment after final received 11/24/2003.
2. ☒ The allowed claim(s) is/are 1-21.
3. ☒ The drawings filed on 28 July 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f):
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.
5. ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.
- (a) ☐ The translation of the foreign language provisional application has been received.
6. ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE**

7. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
8. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No. _____.
- (b) ☐ including changes required by the proposed drawing correction filed _____, which has been approved by the Examiner.
- (c) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No. _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the margin according to 37 CFR 1.121(d).

9. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- 1 ☐ Notice of References Cited (PTO-892)
- 2 ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3 ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No. _____
- 4 ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
- 5 ☐ Notice of Informal Patent Application (PTO-152)
- 6 ☐ Interview Summary (PTO-413), Paper No. _____
- 7 ☐ Examiner's Amendment/Comment
- 8 ☒ Examiner's Statement of Reasons for Allowance
- 9 ☐ Other


Minhloan Tran
Primary Examiner
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REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance:

Claims 1-13 are allowed over the references of record because none of these references disclosed or can be combined to yield the claimed invention such as an integrated power device comprising a power component including a first high-voltage region, a low-voltage region, a first unidirectional element and a second unidirectional element connected together between said first high-voltage region and said low-voltage region, a common intermediate node defined by said first and said second unidirectional elements, biasing means connected between said common intermediate node and a second high-voltage region said biasing means configured to connect the intermediate node directly to the second high-voltage region; and a control circuit including the second high-voltage region, as recited in claim 1.

Claims 14-20 are allowed over the references of record because none of these references disclosed or can be combined to yield the claimed invention such as an integrated power device comprising a power circuit, component, or device comprising a first high-voltage region, a low-voltage region, first and second unidirectional elements connected together between the first high-voltage region and the low-voltage region to define a common intermediate node, a biasing circuit connected between the common intermediate node and a second high-voltage region, the biasing circuit comprising a contact pad electrically connected to the common intermediate node, an electrical contact region formed on the second high-voltage region, an electrical connection line

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having a first end connected to the contact pad and a second end connected to the electrical contact region, an edge structure formed in the power circuit and comprising an equipotential annular region surrounding the power circuit, the contact pad of the biasing circuit being set on top of the equipotential annular region, and a control circuit that includes the second high-voltage region. The just-recited limitations are common to all of claims 14-20.

Claim 21 is allowed over the references of record because none of these references disclosed or can be combined to yield the claimed invention such an integrated power device comprising as a power component including a first high-voltage region, a low-voltage region, a first unidirectional element and a second unidirectional element connected together between said first high-voltage region and said low-voltage region, said first and said second unidirectional elements defining a common intermediate node; the power component formed in a first chip; biasing means connected between said common intermediate node and second high-voltage region; a control circuit including a high-voltage region, the control circuit formed in a second chip that is set on top of and fixed to the first chip, wherein the second chip comprising a substrate fixed to the low-voltage region by means of an adhesive layer made of insulating material.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas L Dickey whose telephone number is 703-308-0980. After February 4, 2004, this telephone number will change to (571) 272-1913. The examiner can normally be reached on Monday through Thursday 8 AM to 6 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (703) 308-6601. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 306-3431.

tld
12/2003